

[illegible]

```
SSSSSSSS  TTTTTTTTT  RRRRRRRR  UU      UU  CCCCCCCC  DDDDDDDD  EEEEEEEEE  FFFFFFFFFF
SSSSSSSS  TTTTTTTTT  RRRRRRRR  UU      UU  CCCCCCCC  DDDDDDDD  EEEEEEEEE  FFFFFFFFFF
SS      TT      RR      RR  UU      UU  CC      CC  DD      DD  EE      FF
SS      TT      RR      RR  UU      UU  CC      CC  DD      DD  EE      FF
SS      TT      RR      RR  UU      UU  CC      CC  DD      DD  EE      FF
SSSSSS  TT      RRRRRPRR  UU      UU  CC      CC  DD      DD  EEEEEEEE  FFFFFFFF
SSSSSS  TT      RRRRRRRR  UU      UU  CC      CC  DD      DD  EEEEEEEE  FFFFFFFF
SS      TT      RR  RR  UU      UU  CC      CC  DD      DD  EE      FF
SS      TT      RR  RR  UU      UU  CC      CC  DD      DD  EE      FF
SS      TT      RR  RR  UU      UU  CC      CC  DD      DD  EE      FF
SSSSSSSS  TT      RR      RR  UUUUUUUUU  CCCCCCCC  DDDDDDDD  EEEEEEEEE  FF
SSSSSSSS  TT      RR      RR  UUUUUUUUU  CCCCCCCC  DDDDDDDD  EEEEEEEEE  FF
                                         ....
                                         ....
                                         ....
                                         ....
```

```
RRRRRRRR  EEEEEEEEE  QQQQQQ
RRRRRRRR  EEEEEEEEE  QQQQQQ
RR      RR  EE      QQ      QQ
RR      RR  EE      QQ      QQ
RR      RR  EE      QQ      QQ
RRRRRRRR  EEEEEEEE  QQ      QQ
RRRRRRRR  EEEEEEEE  QQ      QQ
RR  RR  EE      QQ      QQ
RR  RR  EE      QQ      QQ
RR      RR  EE      QQ      QQ
RR      RR  EE      QQ      QQ
RR      RR  EEEEEEEEE  QQQQ  QQ
RR      RR  EEEEEEEE  QQQQ  QQ
```

-----  
STRUCDEF -- DECLARATION FILE FOR DATA STRUCTURE DEFINITION  
AND ACCESS MACROS USED IN THE VAX DEBUGGER  
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Version: 'V04-000'

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WRITTEN BY  
Bert Beander August, 1981.

MODULE FUNCTION:  
This REQUIRE file contains all macros used in defining and accessing  
data structures (BLISS BLOCKS) in the VAX Debugger. These symbolic  
names should always be used in BLISS Field-References.



```

A2_    = 16, 0, 0 %;      ! Address of byte 2
A3_    = 24, 0, 0 %;      ! Address of byte 3

L_     = 0, 32, 0 %;      ! Longword
W_     = 0, 16, 0 %;      ! Word, zero-extended
B_     = 0, 8, 0 %;       ! Byte, zero-extended

W0_    = 0, 16, 0 %;      ! Word 0 zero-extended
W1_    = 16, 16, 0 %;     ! Word 1 zero-extended

B0_    = 0, 8, 0 %;       ! Byte 0 zero-extended
B1_    = 8, 8, 0 %;       ! Byte 1 zero-extended
B2_    = 16, 8, 0 %;      ! Byte 2 zero-extended
B3_    = 24, 8, 0 %;      ! Byte 3 zero-extended

V_(P,S) = P, %IF %NULL(S) %THEN 1 %ELSE S %FI, 0 %, ! Unsigned bit field

V0_(P,S) = P, %IF %NULL(S) %THEN 1 %ELSE S %FI, 0 %, ! Bits in B0_
V1_(P,S) = (P+8), %IF %NULL(S) %THEN 1 %ELSE S %FI, 0 %, ! Bits in B1_
V2_(P,S) = (P+16), %IF %NULL(S) %THEN 1 %ELSE S %FI, 0 %, ! Bits in B2_
V3_(P,S) = (P+24), %IF %NULL(S) %THEN 1 %ELSE S %FI, 0 %, ! Bits in B3_

SW_    = 0, 16, 1 %;      ! Word, sign-extended
SB_    = 0, 8, 1 %;       ! Byte, sign-extended

SW0_   = 0, 16, 1 %;      ! Word 0 sign-extended
SW1_   = 16, 16, 1 %;     ! Word 1 sign-extended

SB0_   = 0, 8, 1 %;       ! Byte 0 sign-extended
SB1_   = 8, 8, 1 %;       ! Byte 1 sign-extended
SB2_   = 16, 8, 1 %;      ! Byte 2 sign-extended
SB3_   = 24, 8, 1 %;      ! Byte 3 sign-extended

SV_(P,S) = P, %IF %NULL(S) %THEN 1 %ELSE S %FI, 1 %, ! Signed bit field

SV0_(P,S) = P, %IF %NULL(S) %THEN 1 %ELSE S %FI, 1 %, ! Bits in B0_
SV1_(P,S) = (P+8), %IF %NULL(S) %THEN 1 %ELSE S %FI, 1 %, ! Bits in B1_
SV2_(P,S) = (P+16), %IF %NULL(S) %THEN 1 %ELSE S %FI, 1 %, ! Bits in B2_
SV3_(P,S) = (P+24), %IF %NULL(S) %THEN 1 %ELSE S %FI, 1 %, ! Bits in B3_

```

```

! END OF STRUCDEF.REQ

```

0400 AH-BT13A-SE  
VAX/VMS V4.0

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REQ

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LIS

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REQ